

MAR'O 7 2005 TRANSMITTAL OF FORMAL DRAWINGS

Docket No. 6065-76257

Re Application Of

Barry W. Jones

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
09/511,526	02/23/2002	Philpott, Justin M.	24628	2665	4196

Invention:

TIMESLOT INTERCHANGE CIRCUIT SUPPORTING PCM, ADPCM, AND MULTIPLE DATA CHANNEL CONNECTIVITY TO T1 AND E1 CIRCUITS

Address to:

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Transmitted herewith are:

- 3 sheets of formal drawing(s) for this application.
- Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

Signature

Jon P. Christensen Reg. No. 34,137 Dated: March 3, 2005

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

03/0/3/2005

(Date)4

Signature of Person Mailing Correspondence

Abby Boone

Typed or Printed Name of Person Mailing Correspondence